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MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE::MADANAPALLE (UGC AUTONOMOUS)

(Affiliated to JNTUA, Anantapur & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution

ACADEMIC SECTION - NOTIFICATION DATE: 23.08.2015

M.Tech. II Semester II Mid – term Test Schedule September – 2015

M.Tech. II Semester (2014 Admitted Batch)

Academic Year - 2014-15

TIMINGS: FN: 10:30 AM to12:30 PM AN: 02:30 PM to 04:30 PM

DATE / DAY		EPS	SPS	MD (2 nd Shift)	DECS	VES	CSE
02.09.2015 Wednesday	FN	HVDC & Flexible A.C. Transmission Systems	HVDC & Flexible A.C. Transmission Systems	Advanced Mechanisms	Embedded System Design	Memory Technology	Data Ware housing and Data Mining
	AN	Machine Modeling Analysis	Solar Power System Design Methodology	Mechanical vibration	Image & Video Processing	FPGA Architecture & Application	Java and Web Technologies
03.09.2015 Thursday	FN	EHVAC Transmission	Design of Utility Scale Solar Power System	Engineering Optimization Techniques	Detection and Estimation Theory	Real time Operating Systems	Mobile Computing
	AN	Advanced Power System Protection	Smart Grid Design and Analysis	Experimental Stress Analysis	Wireless Communications	DSP Architecture and Applications	Advance Computer networks
04.09.2015 Friday	FN	Computer Applications in Power Systems	Design of Building Integrated Solar Power System	Finite Element Analysis	Speech Processing	ASIC Design	Software Quality Assurance and Testing
	AN	Energy Auditing, Conversion and Management	Energy Conservation and Management	Robotics	Wireless Sensor Communication	Design & Testability	Cloud computing

Note: All HODs are requested to circulate among the faculty in the department.

Copy to:

EEE

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Madanapalle Institute of Technology & Science
P.O. No: 14, Kadiri Road, Angallu

MADANAPALLE - 517 325, A.P.

EC To:

2. SAO(G) SAO (A&P)

1. All HODs:

S.P.O AO(Systems)

s)

Dean(R&D)

Dean(Academic)

TC

Librarian

SO TI Dy.Warden

3. Copy to HOD's to be read in M.Tech II Semester Class Rooms

4. Department & Academic Section Notice Boards